

Figure 1  
(Prior Art)

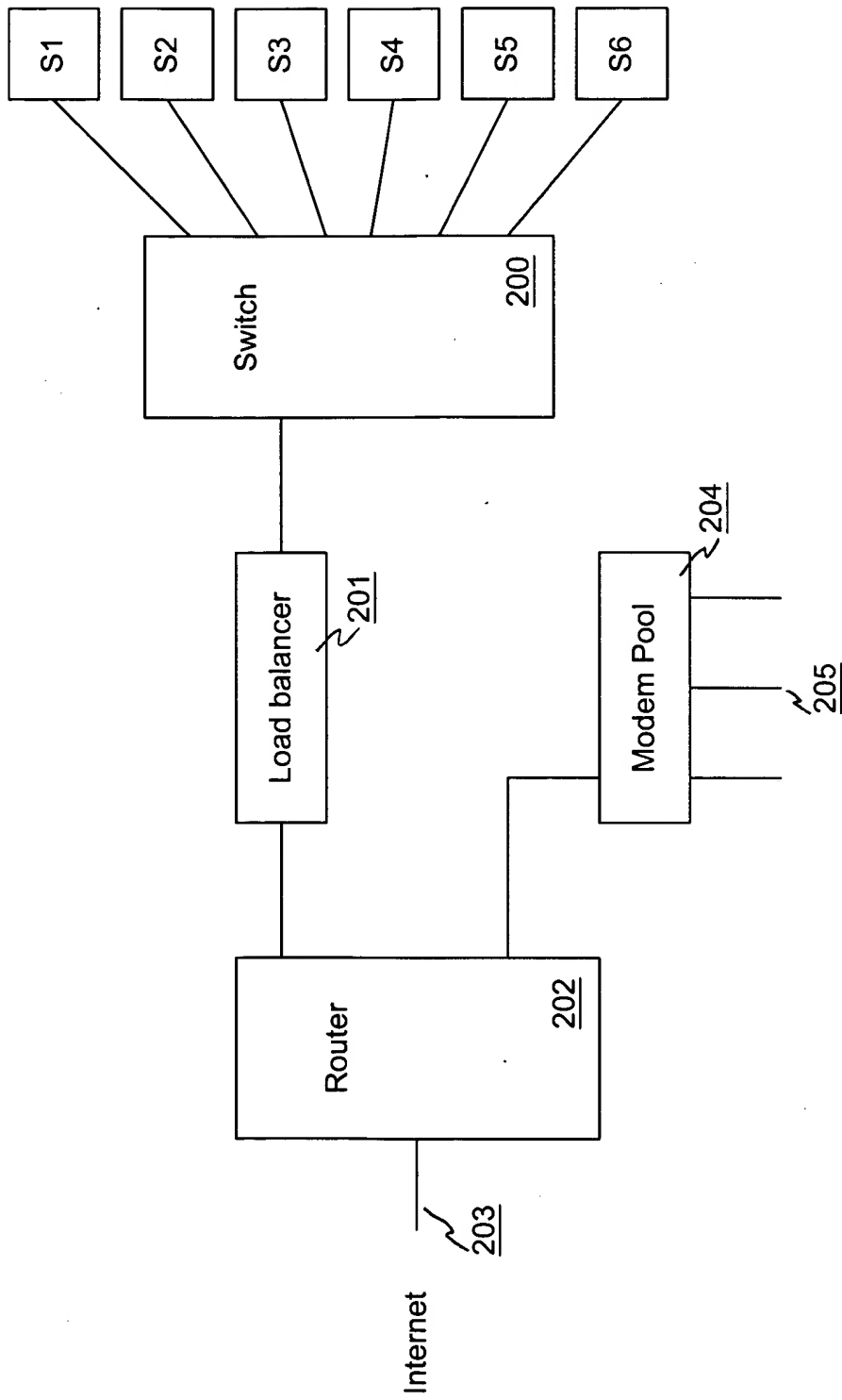


Figure 2  
(Prior Art)

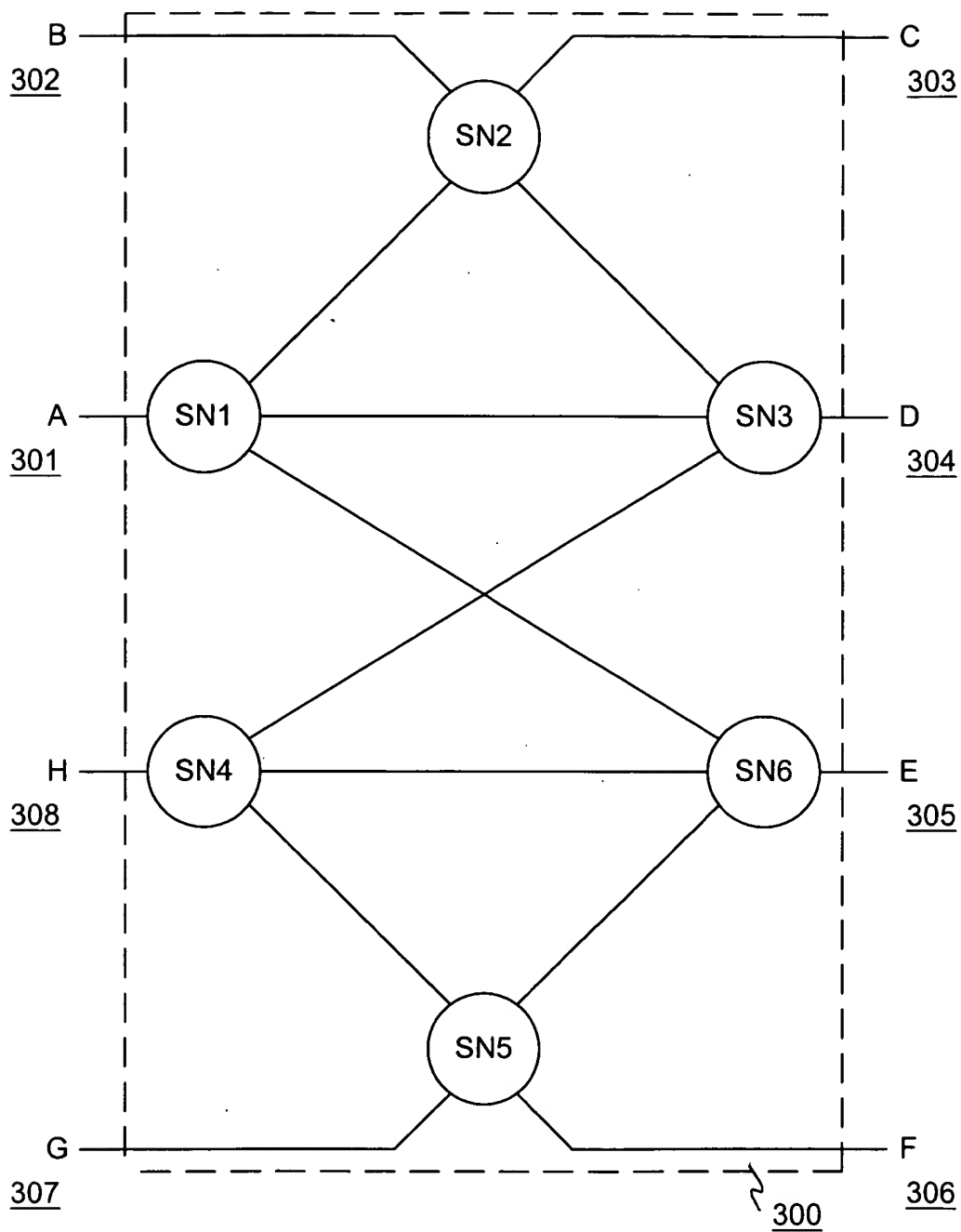


Figure 3

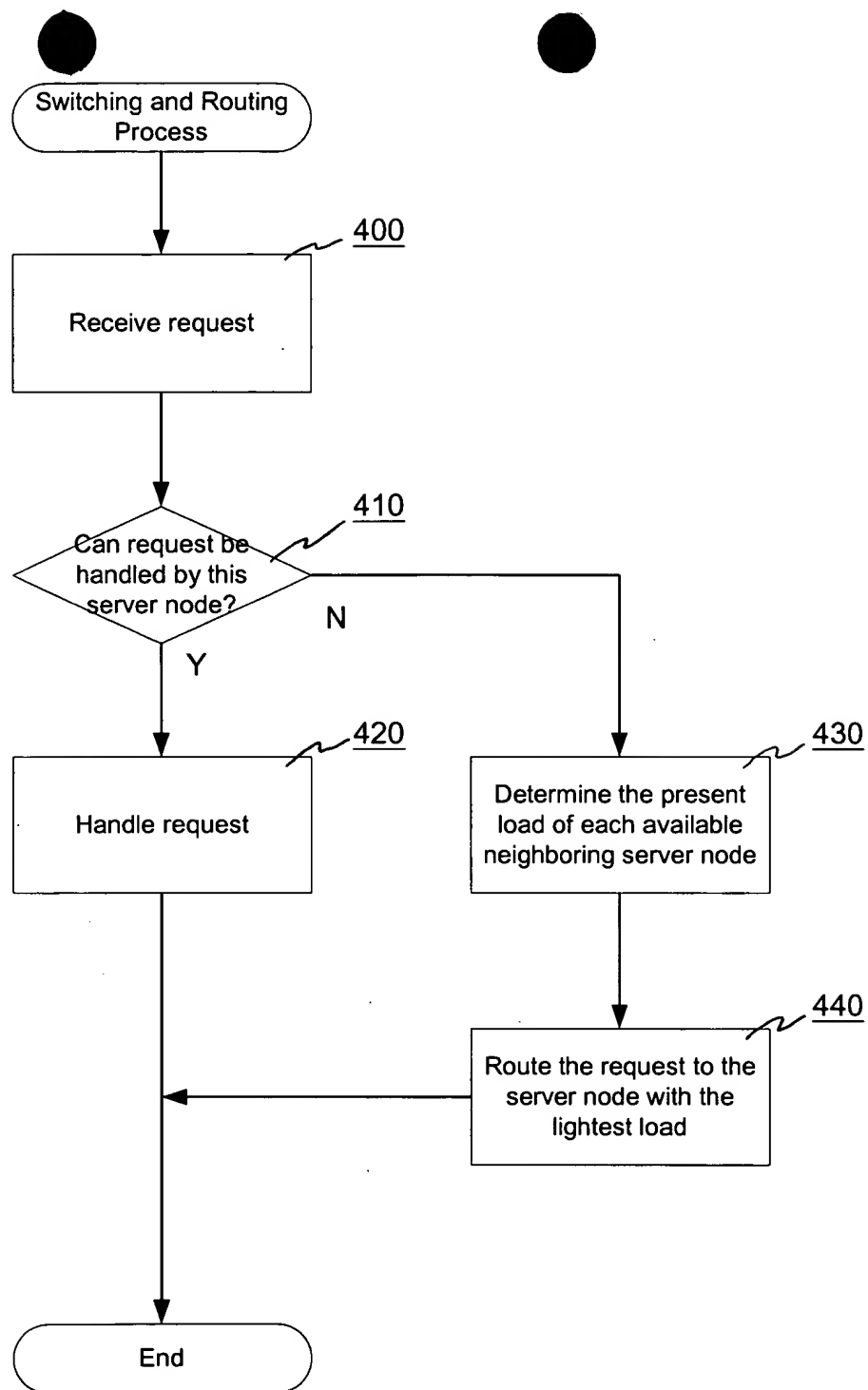


Figure 4

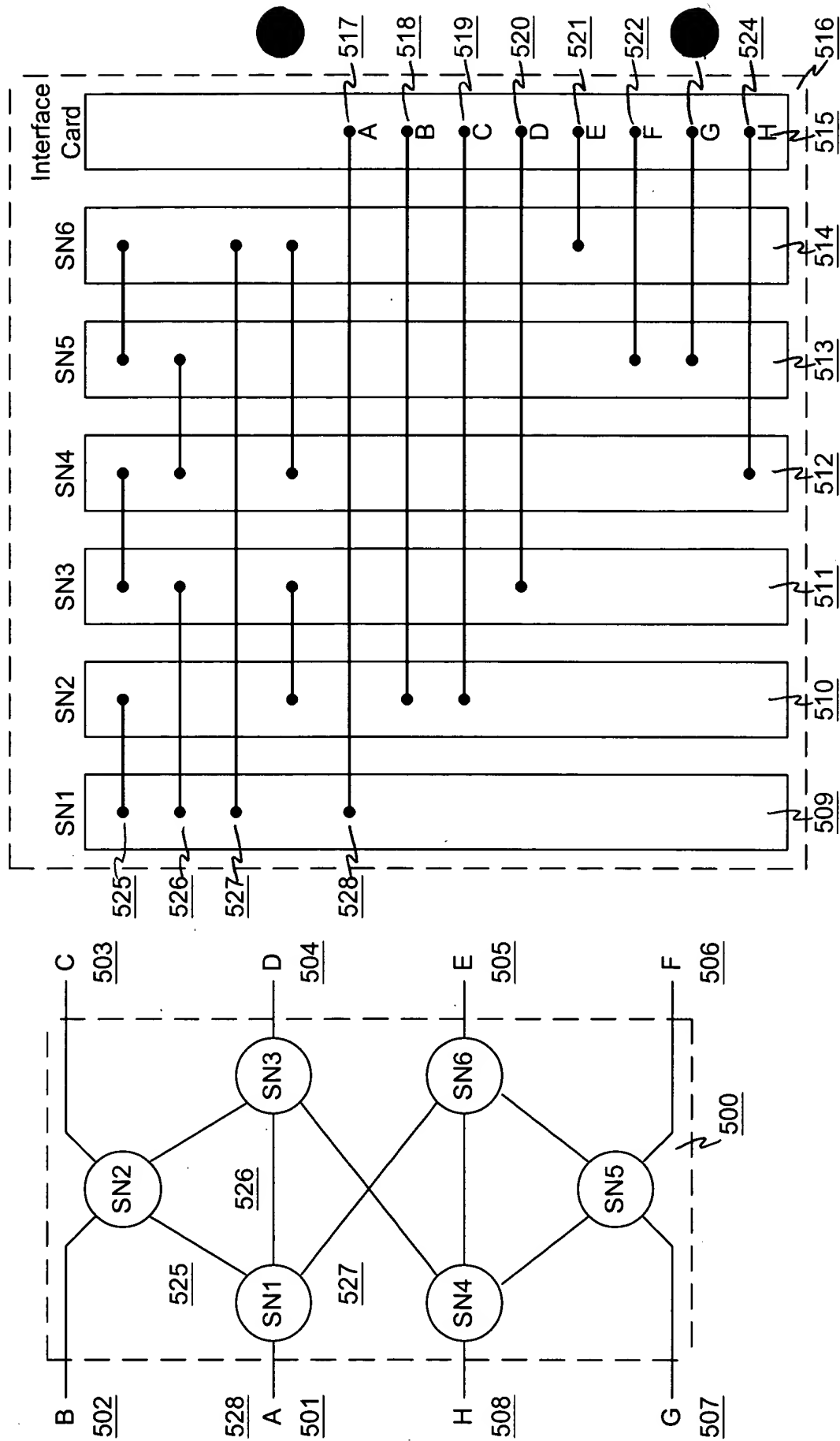


Figure 5

Figure 6 is a schematic diagram of a 3D network structure. It shows four identical blocks (Block 610, Block 620, Block 630, Block 640) arranged in a 2x2 grid. Each block contains a network of six nodes (SN1, SN2, SN3, SN4, SN5, SN6) connected by solid lines. The blocks are interconnected by dashed lines. Labels A, B, C, D, E, F, G, H are placed at various points along the connections and within the blocks. Two large black circles are at the top of the diagram.

### Figure 6

The diagram illustrates a multi-block system architecture. It consists of four identical blocks, labeled Block1, Block2, Block3, and Block4, arranged vertically. Each block contains six vertical bars (representing data paths or channels) and a control unit (701) with eight inputs labeled A through H. The control units are connected to a common bus system (702) via a switch matrix (703). The switch matrix routes the signals from the control units to the common bus system, allowing for flexible data flow between the blocks and the bus.

### Figure 7

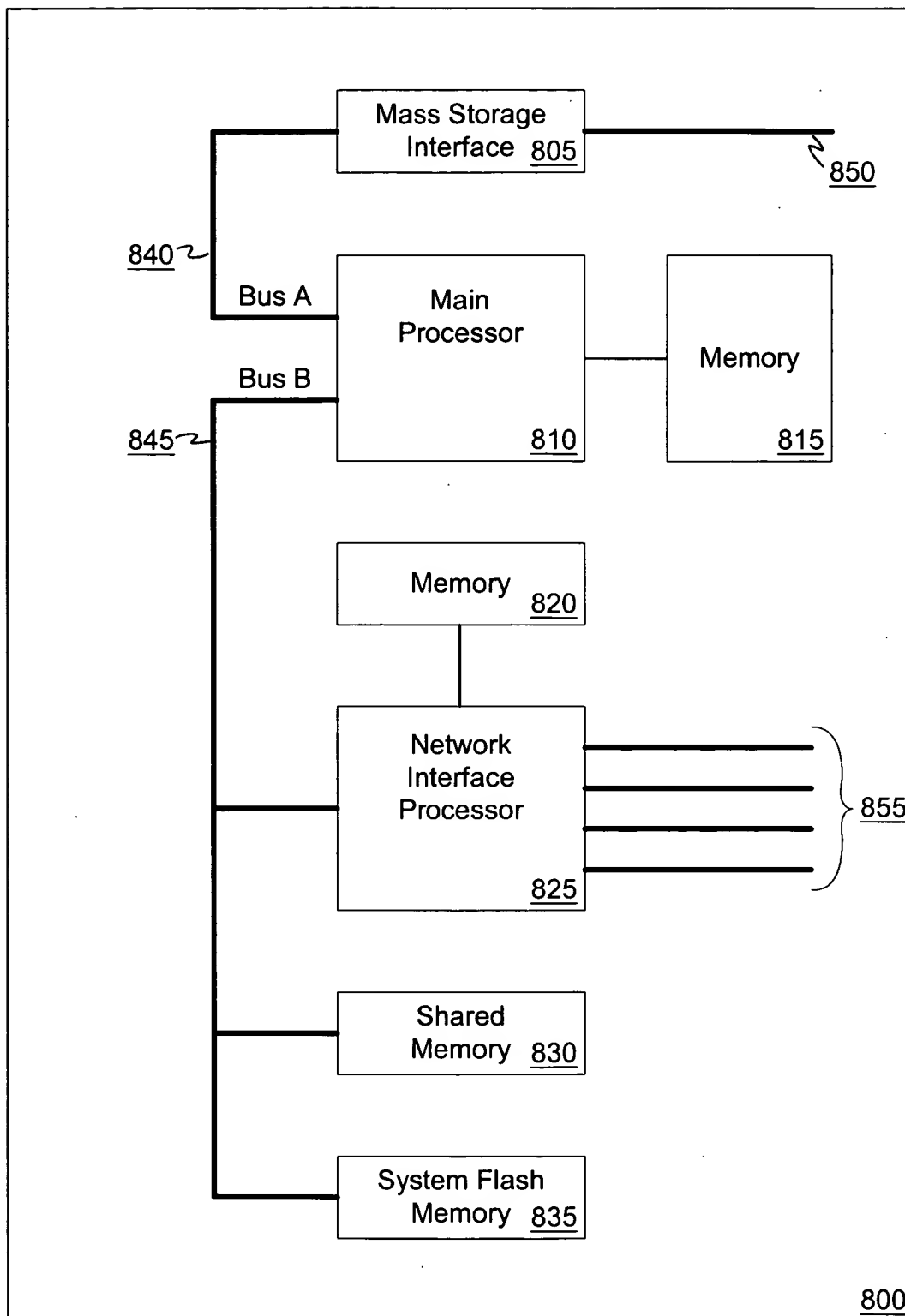


Figure 8